



PATENT  
Att'y Dkt.: 2207/10607

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

SOLOMON et al

Serial No.: 09/892,566

Filed: June 28, 2001

For: POWER REDUCTION FOR PROCESS  
FRONT-END BY CACHING DECODED  
INSTRUCTIONS

Assignee: Intel Corporation

Examiner: G.J. Portka

Art Unit: 2188

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Preliminary to examination of the above-identified application, please amend the application as follows: